

## AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning at page 8, line 4, with the following amended paragraph:

Fig. 5 is a flow diagram illustrating transition of adjacent signals for a five-signal group. Fig. 5 specifically illustrates the logic involved in the block diagram and the sensing and delay circuits of Fig. 4. As in a three-signal group, contention is provided for a five-signal group using a sensing and delay circuit or similar logic. Other multiple signal groups can also make use of such logic and similar sensing and delay circuit (logic). In this example, the sensing and delay circuit receives five signals, signals 1, 2, 3, 4 and 5, step 500. Signals 1, 2, 3, 4, and 5 in order are adjacent to one another in the group. In other words, signal 1 is adjacent to signal 2; signal 2 is adjacent to signal 3; signal 3 is adjacent to signal 4; and signal 4 is adjacent to signal 5. Signals 1 and 2 are sensed with one another, step 505. Signals 2 and 3 are sensed with one another, step 510. Signals 3 and 4 are sensed with one another, step 515. Signals 4 and 5 are sensed with one another, step 520. A determination is made as to whether ~~Signals~~ signals 1 and 2 are transitioning (switching) at the same time, step 525. If step 525 is determined to be “yes” then ~~Signal~~ signal 1 is delayed, step 530. If step 525 is determined to be “no” then ~~Signal~~ signal 1 is not delayed, step 535. A determination is made as to whether adjacent signals 4 and 5 are transitioning at the same time, step 540. If step 540 is determined to be “yes” then signal 5 is delayed, step 545. Since signal 3 is the middle signal of the five-signal group and is directly adjacent to signals 2 and 4, signal 3 is delayed if signal 3 transitions at the same time as either signal 2 or signal 4. A separate determination is made as to whether ~~signals2~~ signals 2 and 3 are transitioning at the same time, step 555. Another determination is made as to whether signals 3 and 4 are transitioning at the same time, step 560. If either step 555 or step 560 is “yes,” signal 3 is delayed, step 565. If both step 555 and step 560 are “no” then ~~Signal~~ signal 3 is not delayed, step 570.